

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

<b>Title of Invention</b>	High Voltage Circuits Implemented Using Low Voltage Transistors							
Application Number :								
Date :								
First Named Applicant:	Mr. Sanish Koshi JACOB							
Confirmation Number:								
Attorney Docket Number:	TI-37611							
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td><td>Sign. Capacity</td></tr><tr><td>Narendra Reddy Thappeta Registered Number: 41416</td><td>(Naren Thappeta</td><td>Attorney</td></tr></table>			Submitted By:	Elec. Sign.	Sign. Capacity	Narendra Reddy Thappeta Registered Number: 41416	(Naren Thappeta	Attorney
Submitted By:	Elec. Sign.	Sign. Capacity						
Narendra Reddy Thappeta Registered Number: 41416	(Naren Thappeta	Attorney						

Documents being submitted:	Files
us-fee-sheet	txnefiling-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-ids	txnefiling-usidst.xml us-ids.dtd us-ids.xsl
us-request	txnefiling-usrequ.xml us-request.dtd us-request.xsl
us-declaration	decltxn95.tif
application-body	spectxn95-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd fig_01.tif fig_02.tif fig_03.tif fig_04.tif fig_05.tif fig_06.tif fig_07.tif fig_08.tif fig_09.tif fig_10.tif fig_11.tif

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**Comments**

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